

1W, DC/DC Power Converter

SDB01S



Introduction

The SDB01S series are SIP7 package DC/DC converters with tightly regulated single output, and 3KVDC isolation. These converters feature high efficiency, low ripple and noise, short circuit protection, and wide operating temperature range. They are widely used in distributed power system in industrial applications where isolation and voltage converting is needed.

Features

- Rated power: 1W Max
- Input voltage range $\pm 5\%$
- Regulated output
- High efficiency up to 72%
- Isolation voltage 3KVDC
- Operating temperature range: $-40 \sim +85^{\circ}\text{C}$ ambient
- RoHS compliant
- Industrial standard SIP7 package
- Continuous short circuit protection
- Meet UL/EN/IEC 62368-1
- 3 year warranty

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Part numbers

Model Number	Input Voltage [VDC] $\pm 10\%$	Output Voltage (VDC)	Output Current (mA) Max	Output Current (mA) Min	Efficiency (%) Typ.	Capacitive Load (uF) Max.
SDB01S0503	5	3.3	250	25	69	2400
SDB01S0505	5	5	200	20	69	2400
SDB01S0509	5	9	111	12	69	1000
SDB01S0512	5	12	84	9	69	560
SDB01S0515	5	15	67	7	69	560
SDB01S0524	5	24	41	4	69	100
SDB01S1203	12	3.3	250	25	69	2400
SDB01S1205	12	5	200	20	72	2400
SDB01S1209	12	9	111	12	72	1000
SDB01S1212	12	12	84	9	72	560
SDB01S1215	12	15	67	7	72	560
SDB01S2403	24	3.3	250	25	69	2400
SDB01S2405	24	5	200	20	72	2400
SDB01S2409	24	9	111	12	72	1000
SDB01S2412	24	12	83	9	72	560
SDB01S2415	24	15	67	7	72	560

* Only typical models are listed. Other models may be available upon request.

* See MR1S series for 1.5KVDC isolation models.

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Electrical characteristics

Unless otherwise indicated, specifications are measured at $T_A=25^{\circ}\text{C}$, nominal input voltage, full load after warm up.

Parameter	Condition	Min.	Type	Max.	Unit	Note
Input current Full load	V _{IN} =5V		260			
	V _{IN} =12V	-	110	-	mA	
	V _{IN} =24V		57			
Input current No load	V _{IN} =5V		15			
	V _{IN} =12V	-	8	-	mA	
	V _{IN} =24V		4			
Reflected ripple current		-	15	-	mA	
Output voltage accuracy		-	-	±3	%	
Line regulation For V _{IN} change of ±1%		-	-	±0.25	%	
Load regulation I _{OUT} =10% to 100% of I _{OUT, rated}	V _{OUT} =3.3V Others	-	-	±3 ±2	%	
Temperature coefficient	Full load	-	±0.02	-	%/°C	
Output ripple and noise 20MHz bandwidth	V _{OUT} =15, 24V Others	-	80 50	150 100	mVp-p	
Output short circuit protection		Continuous, automatic recovery				
Input filter		Capacitor				
Hot plug		None				

* Operating with less than 10% of rated load will not cause permanent damage to the converters, but the performances data may not fall into the specifications, and reliable operating is not assured.

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General Specifications

Parameters	Condition	Min.	Typ.	Max.	Unit	Notes
Isolation voltage 1 minute, leakage current 1mA max	Input to Output	3000	-	-	VDC	
Isolation resistance 500VDC	Input to Output	1000	-	-	M ohm	
Isolation capacitance 100KHz, 0.1V	Input to Output	-	20	-	pF	
Operating temperature	See "Derating Curve"	-40	-	+85	°C	
Storage temperature		-55	-	+125	°C	
Temperature rise at full load		-	25	-	°C	
Storage humidity		-	-	95	%RH	
Switching frequency	Full load	-	250	-	KHz	
Pin soldering resistance 1.5mm away from case for 10 sec		-	-	300	°C	
Vibration		10-150Hz, 5G, 0.75mm along X, Y and Z				
Case material		Black plastic UL94-V0				
Cooling method		Free air convection				
Design based on standards		UL/EN/IEC 62368-1				
Safety certifications		EN/IEC 62368-1				

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EMC	Emissions Immunity	CISPR32, EN55032 Class B* IEC/EN61000-4-2 Air $\pm 8\text{kV}$, Contact $\pm 4\text{kV}$, perf. Criteria B
MTBF	MIL-HDBK-217F	>3,500,000 Hours, $T_A=25^\circ\text{C}$
Size & Weight		19.65 x 6.0 x 10.16 mm, 2.1g Typ.
Weight		1.6 Typ.

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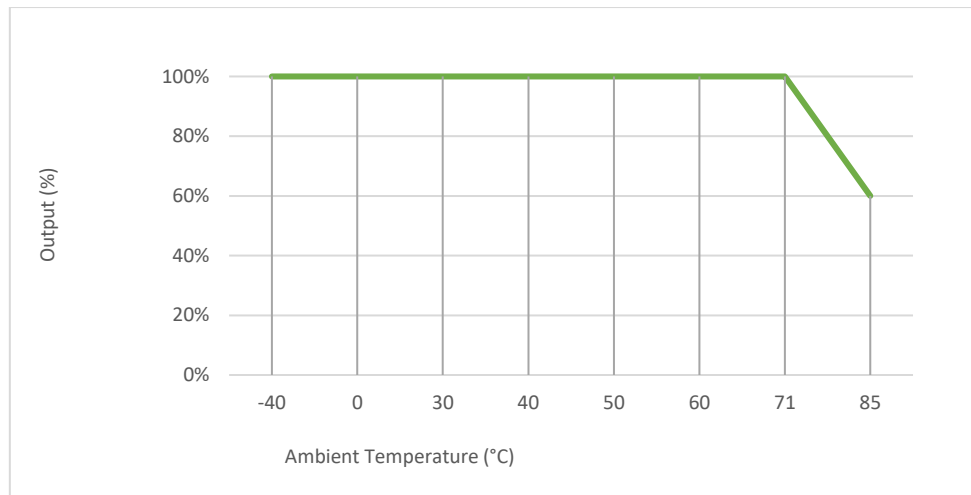
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Characteristics Curves

Derating Curve

Output vs Ambient Temperature



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Recommended External Circuit

Typical Application Circuit

*Typical application circuit is to further lower the input and output ripple. It is not required for general use.

*Recommended component specifications are typical values. Excessive external capacitive load may cause startup problem.



Figure 1: Typical External circuit

Recommended component spec

Input voltage	5V	12V	24V
C _{IN}	4.7uF, 16V	2.2uF, 25V	1uF, 50V

Recommended component spec

Output voltage	3.3, 5V	9, 12V	15, 24V
C _{OUT}	10uF, 16V	2.2uF, 25V	1uF, 50V

EMC Enhancement for EN50032 Class B

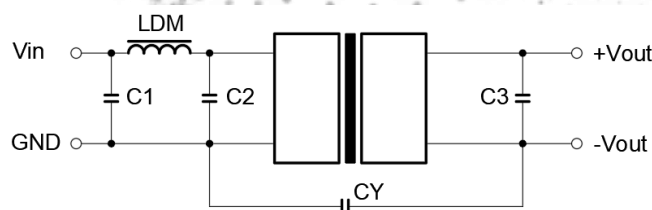


Figure 2: Circuit for EMC Enhancement

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Recommended component spec

Component	LDM	C1, C2	CY
$V_{IN}=5V$	6.8uH	4.7uF, 25V	1nF, 4KV
$V_{IN}=12, 24V$	6.8uH	4.7uF, 50V	270pF, 3KV

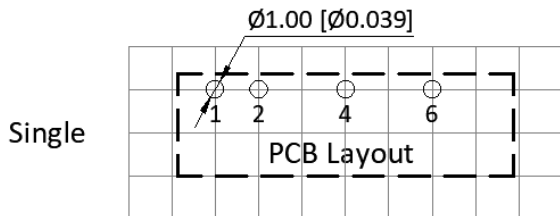
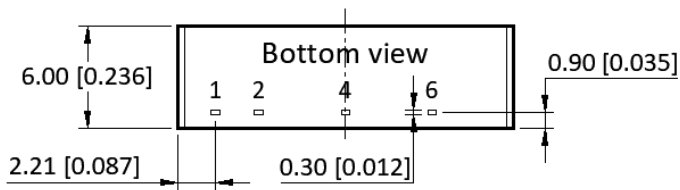
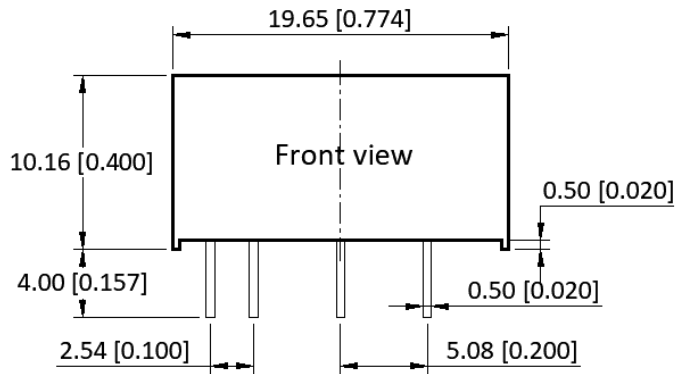
*C3 refer to C_{OUT} in [Table 2]

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Mechanical Specifications



Pin Definition

Pin #	Single Out	
1	V _{IN}	
2	GND	
4	0V	
6	+V _{OUT}	

* Unless otherwise specified unit:

mm [inch]

* General tolerance: ± 0.50 [± 0.020]

* Pin thickness: ± 0.10 [± 0.004]

* Footprint grid 2.54 x 2.54 mm

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Ordering information

Ordering can be done via www.summit-electronics.com or via info@summit-electronics.com. Please contact us for more information. Customisation of the product is available on request.

Technical support

For all product questions please contact us via info@summit-electronics.com

Document revision

Rev	Date	Changes
2025v0.1	27-08-2025	First issue of document